Est. 1970

## DESCRIPTION

The LSC oscillator is designed especially for applications requiring low power consumption. The design consists of a Statek miniature Tuning Fork quartz crystal and a CMOS compatible integrated circuit in a ceramic package.

## FEATURES

Ultra-low power consumption
■ Typical start-up time of 500ms
Low aging

- CMOS compatible
- Typical rise and fall times of 25 ns
- 5 V operation standard

■ 3.3 V operation also available

- Optional Tri-State output
- Low cost solution

Designed, manufactured, and tested in the USA

## APPLICATIONS

Industrial, Computer \& Communications

- General purpose clock oscillator
- Data logger
- Remote sensor
- Real time clock
$\square$ Medical test and diagnostics
- Portable field communication

LSC OSCILLATOR<br>30 kHZ to 400 kHz

Low Power Surface Mount Crystal Oscillator


PACKAGE DIMENSIONS

TYPICAL
MAXIMUM

| DIM | inches | mm | inches | mm |
| :---: | :---: | :---: | :---: | :---: |
| A | 0.400 | 10.16 | 0.405 | 10.29 |
| B | 0.180 | 4.57 | 0.185 | 4.70 |
| C $^{*}$ | 0.065 | 1.65 | 0.070 | 1.77 |
| D | 0.200 | 5.08 | 0.205 | 5.21 |
| E | 0.080 | 2.03 | 0.085 | 2.16 |
| F | 0.050 | 1.27 | 0.058 | 1.47 |
| G | 0.055 | 1.40 | 0.063 | 1.60 |

Termination material is Au over Ni (SM1), solder dip (SM3) also available.
*SM1 Termination; SM3 $=0.075$ in. ( 1.91 mm ) Max.

## SUGGESTED LAND PATTERN




## SPECIFICATIONS: LSC 30 kHz to 400 kHz

Specifications are typical at $25^{\circ} \mathrm{C}$ unless otherwise noted. Specifications are subject to change without notice.

| Supply Voltage $^{1}$ | $5 \mathrm{~V} \pm 10 \%$ |
| :--- | :--- |
|  | $3.3 \mathrm{~V} \pm 10 \%$ |
| Calibration Tolerance | $\pm 10 \mathrm{ppm}(0.001 \%)$ |
|  | $\pm 25 \mathrm{ppm}(0.0025 \%)$ |
|  | $\pm 100 \mathrm{ppm}(0.01 \%)$ |

Frequency Stability Over Temperature ${ }^{2}$

| $0^{\circ} \mathrm{C}$ to $+50^{\circ} \mathrm{C}$ | $\pm 25 \mathrm{ppm}$ | Typ. | $(0.0025 \%)$ |
| :--- | :--- | :--- | :--- |
| $-10^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | $\pm 40 \mathrm{ppm}$ | MAX. | $(0.004 \%)$ |
|  | $\pm 70 \mathrm{ppm}$ | Typ. | $(0.007 \%)$ |
|  | $\pm 100 \mathrm{ppm}$ MAX. | $(0.01 \%)$ |  |
| Voltage Coefficient | $\pm 1 \mathrm{ppm} / \mathrm{V}$ |  |  |
| Aging, first year | $\pm 2 \mathrm{ppm}$ |  |  |
| Shock | $5000 \mathrm{~g}, 0.3 \mathrm{~ms}, 1 / 2$ sine |  |  |
| Vibration | $20 \mathrm{~g} \mathrm{RMS}, 10-2000 \mathrm{~Hz}$ |  |  |
| Operating Temp. Range | $-10^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C} \quad$ (Commercial) |  |  |
|  | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | (Industrial) |  |
|  | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ (Military) |  |  |

1. Contact the factory for lower voltage.
2. Does not include calibration tolerance. Positive variations small compared to negative variations.

Current Consumption*
$2.8 \mu \mathrm{~A} \quad(32.768 \mathrm{kHz})$

* $V_{D D}=3.3 \mathrm{~V}$ and 10 pF load.
$8.0 \mu \mathrm{~A}(100.0 \mathrm{kHz})$


## TRISTATE/DISABLE OPTIONS (T/N)

Statek offers two enable/disable options: T and N . The Tversion has a Tri-State output and continues oscillating internally when the output is put into the high Z state. The N -version does not have PIN 1 connected internally and so has no Tri-State/Disable capability. The following table describes the Tri-State/Disable option T.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage $\mathrm{V}_{\mathrm{Do}}$
Storage Temperature
Process Temperature
-0.5 V to 7 V
$-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$
$260^{\circ} \mathrm{C} 20 \mathrm{sec}$.

## ELECTRICAL CHARACTERISTICS

 LSC 32.768 kHzAll parameters are measured at $25^{\circ} \mathrm{C}$ with a $10 \mathrm{M} \Omega$ and $10 p F$ load with $\mathrm{V}_{\mathrm{DD}} 3.3 \mathrm{~V}$.

| SYMBOL | PARAMETER | MIN. | TYP. | MAX. UNIT |  |
| :---: | :--- | :--- | :--- | :--- | :--- |
| $\mathrm{V}_{\mathrm{OH}}$ | Output Voltage Hi | $\mathrm{V}_{\mathrm{DD}}-0.4$ | $\mathrm{~V}_{\mathrm{DD}}$ |  | V |
| $\mathrm{V}_{\mathrm{OL}}$ | Output Voltage Lo |  | 0 | 0.4 | V |
| SYM | Duty Cycle | 45 | 50 | 55 | $\%$ |
| $\mathrm{t}_{\mathrm{r}}$ | Rise Time (10\%-90\%) |  | 50 | nsec. |  |
| $\mathrm{t}_{\mathrm{f}}$ | Fall Time (10\%-90\%) |  | 50 | nsec. |  |

## PIN CONNECTIONS

| $\frac{\text { Pin }}{1}$ | Connection |
| :---: | :--- |
| 2 | Output Enable or NC |
| 3 | Output |
| 4 | VDD |

## PACKAGING OPTIONS

## LSC -Tray Pack

-16 mm tape, $7^{\prime \prime}$ or $13^{\prime \prime}$ reels
(Reference tape and reel data sheet 10109)

TRISTATE/DISABLE OPTION T FUNCTION TABLE

|  | Tri-State (Pin 1 High*) | Disable (Pin 1 Low) |
| :--- | :--- | :--- |
| Output | Frequency Output | High Z State |
| Internal Osc. | Oscillates | Oscillates |
| Current | Normal | Lower than Normal |

*When PIN 1 is allowed to float, it is held high by an internal pull-up resistor.

HOW TO ORDER LSC SURFACE MOUNT CRYSTAL OSCILLATORS


