

# **LSC OSCILLATOR**

30 kHZ to 400 kHz

Low Power Surface Mount Crystal Oscillator

### **DESCRIPTION**

The LSC oscillator is designed especially for applications requiring low power consumption. The design consists of a Statek miniature Tuning Fork quartz crystal and a CMOS compatible integrated circuit in a ceramic package.

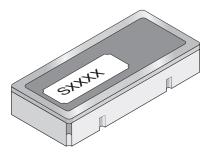
#### **FEATURES**

- Ultra-low power consumption
- Typical start-up time of 500ms
- Low aging
- CMOS compatible
- Typical rise and fall times of 25ns
- 5 V operation standard
- 3.3 V operation also available
- Optional Tri-State output
- Low cost solution
- Designed, manufactured, and tested in the USA

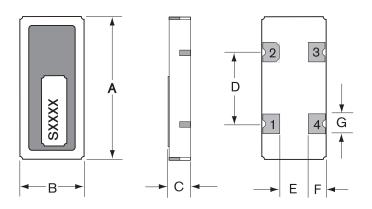
## **APPLICATIONS**

Industrial, Computer & Communications

- General purpose clock oscillator
- Data logger
- Remote sensor
- Real time clock
- Medical test and diagnostics
- Portable field communication



## PACKAGE DIMENSIONS

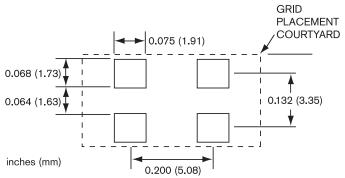


	TYPICAL		MAXIMUM		
DIM	inches	mm	inches	mm	
Α	0.400	10.16	0.405	10.29	
В	0.180	4.57	0.185	4.70	
C*	0.065	1.65	0.070	1.77	
D	0.200	5.08	0.205	5.21	
Е	0.080	2.03	0.085	2.16	
F	0.050	1.27	0.058	1.47	
G	0.055	1.40	0.063	1.60	

Termination material is Au over Ni (SM1), solder dip (SM3) also available.

\*SM1 Termination; SM3 = 0.075 in. (1.91mm) Max.

### SUGGESTED LAND PATTERN



10153 - Rev F







### SPECIFICATIONS: LSC 30 kHz to 400 kHz

Specifications are typical at 25°C unless otherwise noted. Specifications are subject to change without notice.

Supply Voltage<sup>1</sup> 5 V ± 10%

3.3 V ± 10%

Calibration Tolerance ± 10 ppm (0.001%)

± 25 ppm (0.0025%)

± 100 ppm (0.01%)

Frequency Stability Over Temperature<sup>2</sup>

 $0^{\circ}$ C to +50°C  $\pm 25$  ppm Typ. (0.0025%)

± 40 ppm MAX. (0.004%)

Voltage Coefficient ± 1 ppm/V Aging, first year ± 2 ppm

Shock 5000 g, 0.3 ms,1/2 sine Vibration 20 g RMS, 10 - 2000 Hz

Operating Temp. Range -10°C to +70°C (Commercial)

-40°C to +85°C (Industrial) -55°C to +125°C (Military)

- 1. Contact the factory for lower voltage.
- 2. Does not include calibration tolerance. Positive variations small compared to negative variations.

Current Consumption\* 2.8  $\mu$ A (32.768 kHz) \* V<sub>DD</sub> = 3.3 V and 10pF load. 8.0  $\mu$ A (100.0 kHz)

## TRISTATE/DISABLE OPTIONS (T/N)

Statek offers two enable/disable options: T and N. The T-version has a Tri-State output and continues oscillating internally when the output is put into the high Z state. The N-version does not have PIN 1 connected internally and so has no Tri-State/Disable capability. The following table describes the Tri-State/Disable option T.

### **ABSOLUTE MAXIMUM RATINGS**

Supply Voltage  $V_{DD}$  -0.5 V to 7 V Storage Temperature -55°C to +125°C Process Temperature 260°C 20 sec.

### **ELECTRICAL CHARACTERISTICS**

#### LSC 32.768 kHz

All parameters are measured at 25°C with a 10M $\!\Omega$  and 10pF load with  $V_{DD}$  3.3 V.

SYMBOL	PARAMETER	MIN.	TYP.	MAX.	UNIT
$V_{OH}$	Output Voltage Hi	$V_{DD}$ -0.4	$V_{\text{DD}}$		V
V <sub>OL</sub>	Output Voltage Lo		0	0.4	V
SYM	Duty Cycle	45	50	55	%
t <sub>r</sub>	Rise Time (10%-90%)			50	nsec.
t <sub>f</sub>	Fall Time (10%-90%)			50	nsec.

### PIN CONNECTIONS

<u>Pin</u>	Connection
1	Output Enable or NC
2	Ground
3	Output
4	$V_{DD}$

#### **PACKAGING OPTIONS**

LSC -Tray Pack

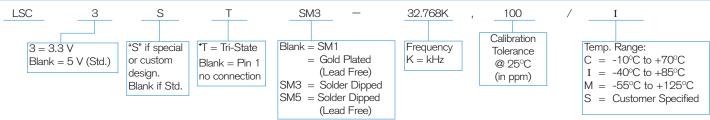
-16mm tape, 7" or 13" reels (Reference tape and reel data sheet 10109)

### TRISTATE/DISABLE OPTION T FUNCTION TABLE

	Tri-State (Pin 1 High*)	Disable (Pin 1 Low)		
Output	Frequency Output	High Z State		
Internal Osc.	Oscillates	Oscillates		
Current	Normal	Lower than Normal		

<sup>\*</sup>When PIN 1 is allowed to float, it is held high by an internal pull-up resistor.

# HOW TO ORDER LSC SURFACE MOUNT CRYSTAL OSCILLATORS



10153 - Rev F





