IT2200J

SMD Temperature Compensated Crystal Oscillator

SMD TCXO using analogue ASIC for compensation and an optional Enable/Disable pin for efficient power management.

Product description

The I(V)T2200J employs an analogue ASIC for the oscillator and a high order temperature compensation circuit in a 2.5 x 2.0 mm size package. The device can be placed in power down mode through a single input pin. During standard operation, power consumption is minimized by operating down to a supply voltage of 1.8V. The I(V)T2200J's high stability, low power consumption, small footprint and powerful compensation method makes it a TCXO ideally suited for demanding GPS mobile applications.

Applications

- Consumer
- Communications
- GPS
- Feature phone

Features

- Excellent phase noise performance
- Frequency slope and perturbation specifications can be customized to the application requirement
- Standard temperature stability choices are ±0.5ppm, ±1ppm, ±1.5ppm and ±2.5ppm over wide temperature ranges

Specifications

1.0 SPECIFICATION REFERENCES

Line	Parameter	Description
1.1	Model description	IT2200J / IVT2200J / IT2200JP
1.2	RoHS compliant	Yes
1.3	Reference number	
1.4	Rakon part number	

2.0 FREQUENCY CHARACTERISTICS

Line	Parameter	Test Condition	Value	Unit
2.1	Frequency		10 to 52	MHz
2.2	Frequency calibration	Offset from nominal frequency measured at 25°C±2°C	±1 max	ppm
2.3	Reflow shift	Two consecutive reflows as per attached profile after 1 hour recovery at 25°C	±1 max	ppm
2.4	Temperature range	The operating temperature range over which the frequency stability is measured	-40 to 85	°C
2.5	Frequency stability over temperature	Referenced to the midpoint between minimum and maximum frequency value over the specified temperature range. Control voltage set to midpont of control voltage (Note 1)	±0.5 to 2.5	ppm
2.6	Frequency slope	Minimum of 1 frequency reading every 2°C over the operating temperature range (Note 1)	0.1 to 1	ppm/°C
2.7	Static temperature hysteresis	Frequency change after reciprocal temperature ramped over the operating range. Frequency measured before and after at 25°C	0.6 max	ppm
2.8	Sensitivity to supply voltage variations	Supply voltage varied ±5% at 25°C	±0.2 max	ppm
2.9	Sensitivity to load variations	±10% load change at 25°C (Note 2)	±0.2 max	ppm
2.10	Long term stability	Frequency drift over 1 year at 25°C	±1 max	ppm

3.0 POWER SUPPLY

Line	Parameter	Test Condition	Value	Unit
3.1	Supply voltage	Nominal supply voltage range	1.8 to 3.3	V
3.2	Current	At maximum supply voltage (Note 2)	2 max	mA

4.0 CONTROL VOLTAGE (VCO)

Line	Parameter	Test Condition	Value	Unit
4.1	Control voltage range	The nominal control voltage value is midway between the minimum and maximum. Voltage control should not exceed the supply voltage +0.2 V or GND. Supply voltage ≤2.3 V	0.3 to 1.5	V
4.2	Control voltage range	The nominal control voltage value is midway between the minimum and maximum. Voltage control should not exceed the supply voltage +0.2 V or GND. Supply voltage >2.3 V	0.4 to 2.4	V
4.3	Frequency tuning	Frequency shift from minimum to maximum control voltages	±10 min	ppm
4.4	Port input impedance	Measured between Control voltage and GND pin	500	kΩ

5.0 OSCILLATOR OUTPUT

Line	Parameter	Test Condition	Value	Unit
5.1	Output waveform	DC coupled clipped sine-wave (Note 3)		
5.2	Output voltage level	At minimum supply voltage (Note 2)	0.8 min	V
5.3	Output load resistance	Refer to test circuit. Typical load 10kΩ	9 to 11	kΩ
5.4	Output load capacitance	Refer to test circuit. Typical load 10pF	9 to 11	pF
5.5	Start up time (amplitude)	Within 90% of specified output level	0.5 max	ms
5.6	Start up time (frequency)	Within ±0.5ppm of steady state frequency	2 max	ms

6.0 POWER DOWN MODE (Enable/Disable Pin)

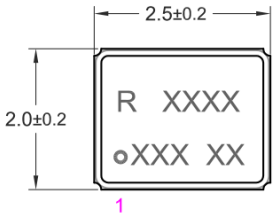
Line	Parameter	Test Condition	Value	Unit
6.1	Power down	RF, Disabled (Minimum GND)	20 max	%Vcc
6.2	Normal operating mode	RF, Enabled (Maximum Vcc)	80 min	%Vcc
6.3	Stand-by current	Typical value <0.01μA	1 max	μA
6.4	Start up time (amplitude)	Within 90% of specified output level	0.5 max	ms
6.5	Start up time (frequency)	Within ±0.5ppm of steady state frequency	2 max	ms

7.0 SSB PHASE NOISE

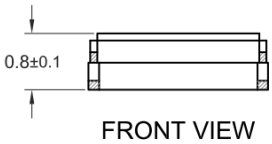
Line	Parameter	Test Condition	Value	Unit
7.1	SSB phase noise power density at 1Hz offset	Typical value for a 38.4 MHz oscillator at 25°C	-62	dBc/Hz
7.2	SSB phase noise power density at 10Hz offset	Typical value for a 38.4 MHz oscillator at 25°C	-86	dBc/Hz
7.3	SSB phase noise power density at 100Hz offset	Typical value for a 38.4 MHz oscillator at 25°C	-109	dBc/Hz
7.4	SSB phase noise power density at 1kHz offset	Typical value for a 38.4 MHz oscillator at 25°C	-132	dBc/Hz
7.5	SSB phase noise power density at 10kHz offset	Typical value for a 38.4 MHz oscillator at 25°C	-148	dBc/Hz

Drawing Name: I(V)T2200J Model Outline

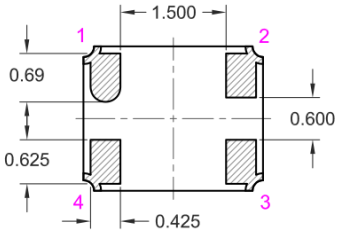
MODEL OUTLINE



TOP VIEW



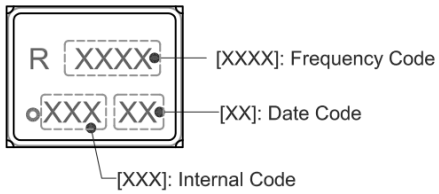
FRONT VIEW



BOTTOM VIEW

LID MARKING *

* Marking information is detailed in the specification.

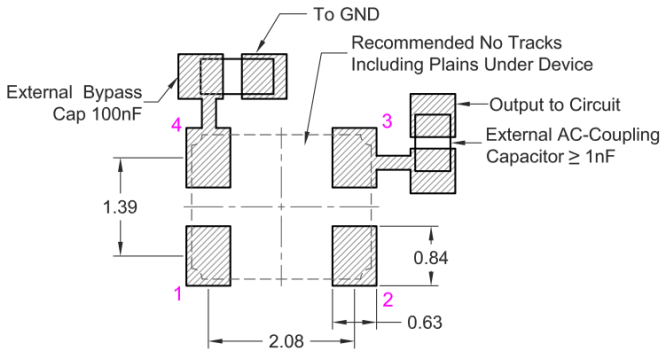


PIN CONNECTIONS

Pin	IT22..J	IVT22..J	IT22..JP
1	NC / GND	VCO	Enable / $\overline{\text{Disable}}$**
2	GND	GND	GND
3	OUTPUT	OUTPUT	OUTPUT
4	VDD	VDD	VDD

** Connect to VDD or floating to enable TCXO.

RECOMMENDED PAD LAYOUT - TOP VIEW



TITLE: I(V)T2200J MODEL	FILENAME: CAT676	TOLERANCES:
RELATED DRAWINGS:	REVISION: D	XX =
	DATE: 28-Aug-14	X.X =
	SCALE: 10 : 1	X.XX = ±0.10
	Millimetres	X.XXX = ±0.05
		X° =
		Hole =

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